(i)	Printed Pages: 2		Roll No				
(ii)	Qu	estions	: 9	Sub	. Code:	0 9 2	
		ş vi		Exam	. Code:	0 0 2	2 8
	B	achelor	of Comp	outer Appli	cations 2nd	Semester	
				(2053)	(\$\frac{1}{2}\)		
		,	COMPUT	TER ORG	ANIZATIO)N	*3
			Pa	per–BCA-	16-202		
Tir	ne Al	llowed	: Three	Hours]	[Maxin	num Marks	: 65
No	te :-	- (1)		t one ques		each Unit an	d the
		(2)	All ques	stions carry	equal mark	s unless spec	ified.
				UNIT-	-I		
1.	(a)		ne design a ip flop.	and logic dia	agram of cor	verting JK fli	p flop
4	(b)	Explai	n half add	ler and full a	adder with th	neir logic diag	gram. 5
2.	(a)	Discus	s addition	and subtrac	tion for sign	magnitude nu	mbers.
¥1	1						6
	(b)	Explai	n Booth's	algorithm v	vith suitable	example.	7
= * **				UNIT-	-II		
3.	(a)		e Interrup y interrupt		n interrupt o	cycle ? Also e	xplain 7
	(b)	What	are micro-	operations	? Design 4-l	oit arithmetic	circuit.
4.	Giv	e the in	struction	set of 8086	/8088 micro	processor.	13

UNIT—III

5.	(a)	memories with the help of	p of circui						
	(b)	 Write assembly language program to perform bas output. 							
6.	(a) Elaborate various features of assembly language. Explaid differences between machine and assembly language.								
	(b)	Compare one pass and tw	vo pass assembler.	8					
		UNIT-	–IV						
7.	Discuss the various physical components of a computer an inspection and diagnostics on PC.								
8.	Define the term "Computer Virus". Explain its various types an preventive and protective measures.								
•		UNIT-	_v						
9.	(a)	What is hit ratio?	الله الإساسي الله الإساسي	1					
	(b)	ion Unit (EU).	2						
	(c)	Define the term "parity".	n half adder a	2					
	(d)	What is DMA?	•	2					
	(e)	What is Cache memory?	s addition and	. 2					
	(f)	Define RTL.	tain Booth's algo	2					
	(g)	What is an Expansion Car	·di?	2					
,			ne Interrupt. W. vinterrupt. vermero o						
		W. Terration							

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